

## ABSTRACT OF THE DISCLOSURE

A method and system of controlling element activation within arrays subject to row and column address signaling. The display, or other form of output device, is configured with circuitry for controlling element activation. The multiplexed row and column signals are utilized for commanding activation of the element, whereas power is constantly provided for driving the display element. By way of example, an embodiment is described wherein the row and column lines can be driven from providing steady-state power to selecting the element for activation or deactivation during the next multiplexing interval. Additional display embodiments are described to suit various applications.